

The block diagram illustrates the system architecture, divided into two main functional blocks. The left block contains three oscillators (Osc1, Osc2, Osc3) and two mixers (RM1, RM2). Each oscillator has associated controls for LDR, Stable, Rate, and Sync. The right block contains a State Variable Filter with various inputs and outputs, including Spk Pwr, Drive, Res, LP, HP, Balance, Out, and Source.

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